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September 2008 Communications of the ACM

Publisher: ACM

Full text available: Digital Edition , Pdf (8.68 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 876, Downloads (12 Months): 2289, Cita

2 [Flush: a reliable bulk transport protocol for multihop wireless network](#)

Sukun Kim, Rodrigo Fonseca, Prabal Datta, Arsalan Tavakoli, David Culler, Stoica

November 2007 SenSys '07: Proceedings of the 5th international conference on sensor systems

Publisher: ACM

Full text available: Pdf (421.18 KB) Additional Information: [full citation](#), [abstract](#), [download](#)

Bibliometrics: Downloads (6 Weeks): 42, Downloads (12 Months): 446, Citation

We present Flush, a reliable, high goodput bulk data transport protocol. Flush provides end-to-end reliability, reduces transfer time, and adapts to changing network conditions. It achieves these properties using end-to-end ...

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Keywords: interference, transport, wireless sensor networks

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3 [Fault-tolerant stream processing using a distributed, replicated file system](#)

YongChul Kwon, Magdalena Balazinska, Albert Greenberg

August 2008 Proceedings of the VLDB Endowment , Volume 1 Issue 1

Publisher: VLDB Endowment

Full text available: Pdf (711.54 KB) Additional Information: [full citation](#), [abstract](#), [download](#)

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We present SGuard, a new fault-tolerance technique for distributed stream processing running in clusters of commodity servers. SGuard is less disruptive to normal stream processing and leaves more resources available for normal stream ...

4 [The Reconfigurable Streaming Vector Processor \(RSVPTM\)](#)

Silviu Ciricescu, Ray Essick, Brian Lucas, Phil May, Kent Moat, Jim Norris, Mark R. Brown

December 2003 MICRO 36: Proceedings of the 36th annual IEEE/ACM International Symposium on Microarchitecture

Publisher: IEEE Computer Society

Full text available:  Pdf (305.63 KB) Additional Information: [full citation](#), [abstract](#), [Bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 60, Citation (

The need to process multimedia data places large computational demands on devices. These multimedia functions share common characteristics: they require data-streaming, performing the same operation(s) on many data ...


5 [EXOCHI: architecture and programming environment for a heterogeneous multithreaded system](#)



[Perry H. Wang](#), [Jamison D. Collins](#), [Gautham N. Chinya](#), [Hong Jiang](#), [Xinmiao Yang](#), [Guei-Yuan Lueh](#), [Hong Wang](#)

June 2007 PLDI '07: Proceedings of the 2007 ACM SIGPLAN conference on Programming Language Design and Implementation

Publisher: ACM

Full text available:  Pdf (242.12 KB) Additional Information: [full citation](#), [abstract](#), [Bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 101, Downloads (12 Months): 742, Citation (

Future mainstream microprocessors will likely integrate specialized accelerators on a single die to achieve better performance and power efficiency. However, programming such a heterogeneous multicore platform, ...

Keywords: GPU, heterogeneous multi-cores, openMP

Also published in:

June 2007 SIGPLAN Notices Volume 42 Issue 6


6 [Visual attention based information culling for Distributed Virtual Environments](#)



[Ashweeni Kumar Beeharee](#), [Adrian J. West](#), [Roger Hubbard](#)

October 2003 VRST '03: Proceedings of the ACM symposium on Virtual reality systems and technologies

Publisher: ACM

Full text available:  Pdf (253.03 KB) Additional Information: [full citation](#), [abstract](#), [Bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 59, Citation (



The central goal in Distributed Virtual Environments (DVEs) is to provide a virtual world that is perceptually plausible for the immersed user. This has to be done in the presence of network issues such as bandwidth limitations and ...

7 [Communications of the ACM: Volume 51 Issue 7](#)



July 2008 Communications of the ACM

Publisher: ACM

Full text available:  Digital Edition ,  Pdf (6.54 MB) Additional Information: [full citation](#), [abstract](#), [Bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 4787, Downloads (12 Months): 6221, Citation (


8 [A simple mechanism for capturing and replaying wireless channels](#)



[Glenn Judd](#), [Peter Steenkiste](#)

August 2005 E-WIND '05: Proceedings of the 2005 ACM SIGCOMM workshop on Endpoints in wireless network design and analysis

Publisher: ACM

Full text available:  Pdf (6.06 MB) Additional Information: [full citation](#), [abstract](#), [Bibliometrics](#)

Physical layer wireless network emulation has the potential to be a powerful and important challenge in physical emulation, and traditional simulation, is a significant challenge. In this paper we examine the possibility of using physical layer emulation to emulate a wireless network channel. In this paper we examine the possibility of using physical layer emulation to emulate a wireless network channel. In this paper we examine the possibility of using physical layer emulation to emulate a wireless network channel.

9 Reducing Design Complexity of the Load/Store Queue

December 2003 MICRO 36: Proceedings of the 36th annual IEEE/ACM International Symposium on Microarchitecture

Full text available: Pdf (174.73 KB) Additional Information: full citation, abstract, ...

With faster CPU clocks and wider pipelines, all relevant microarchitecture accordingly. There have been many proposals for scaling the issue queue hierarchy. However, nothing has been done for scaling the ...

10 Conjoined-Core Chip Multiprocessing

December 2004 MICRO 37: Proceedings of the 37th annual IEEE/ACM International Symposium on Microarchitecture

Full text available: Pdf (369.99 KB) Additional Information: full citation, abstract, ...

Chip Multiprocessors (CMP) and Simultaneous Multi-threading (SMT) are proposed to increase processor efficiency. We believe these two approaches span a wide and viable spectrum. Between these two extremes, there exists ...

¹¹ Balanced Multithreading: Increasing Throughput via a Low Cost Multi

December 2004 MICRO 37: Proceedings of the 37th annual IEEE/ACM International Symposium on Microarchitecture

Full text available: Pdf (197.41 KB) Additional Information: full citation, abstract, ...

A simultaneous multithreading (SMT) processor can issue instructions from multiple threads, allowing it to effectively hide various instruction latencies; this effect increases with the number of simultaneous contexts supported. However, each ...

¹² GPGPU: general purpose computation on graphics hardware


August 2004 SI GGRAPH '04: SIGGRAPH 2004 Course Notes



Full text available: Pdf (63.03 MB) Additional Information: full citation, abstract, etc.

Bibliometrics: Downloads (6 Weeks): 283, Downloads (12 Months): 1460, Cita

The graphics processor (GPU) on today's commodity video cards has ev
and flexible processor. The latest graphics architectures provide tremen
computational horsepower, with fully programmable vertex ...

13 [Queue: Volume 6 Issue 4](#)


 July 2008 Queue
Publisher: ACM



Full text available:  [Digital Edition](#) ,  Pdf (6.13 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 142, Downloads (12 Months): 272, Citati

Flash is growing from mere curiosity to a new tier in the storage hierarc


14 [interactions: Volume 15 Issue 3](#)

 May 2008 interactions
Publisher: ACM

Full text available:  [Digital Edition](#) ,  Pdf (7.95 MB) Additional Information: [full citation](#)


Bibliometrics: Downloads (6 Weeks): 198, Downloads (12 Months): 791, Citati

15 [Evaluation of the field-programmable cache: performance and energ](#)

 Domingo Benítez, Juan C. Moure, Dolores I. Rexachs, Emilio Luque

May 2006 CF '06: Proceedings of the 3rd conference on Computing fronti

Publisher: ACM


Full text available:  Pdf (299.74 KB) Additional Information: [full citation](#), [abstract](#), [info](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 31, Citation (

Many authors have proposed power management techniques for genera
of degraded performance such as lower IPC or longer delay. Some prop
memories because they consume a significant fraction of ...


Keyw ords: adaptive processors, performance evaluation, reconfigurabl
adaptation, static and dynamic energy consumption

16 [Continuous profiling: where have all the cycles gone?](#)

 Jennifer M. Anderson, Lance M. Berc, Jeffrey Dean, Sanjay Ghemawat, Mor
Leung, Richard L. Sites, Mark T. Vandevoorde, Carl A. Waldspurger, William

November 1997 Transactions on Computer Systems (TOCS) , Volume 15 I

Publisher: ACM

Full text available:  Pdf (259.35 KB) Additional Information: [full citation](#), [abstract](#), [info](#)


Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 107, Citatio

This article describes the Digital Continuous Profiling Infrastructure, a s
designed to run continuously on production systems. The system suppo
unmodified executables, and collects profiles ...

Keyw ords: performance understanding, performance-monitoring hardv

[Transparent control independence \(TCI\)](#)

Ahmed S. Al-Zawawi, Vimal K. Reddy, Eric Rotenberg, Halitham H. Akkary
June 2007 I SCA '07: Proceedings of the 34th annual international symposium
Publisher: ACM

Full text available:  Pdf (391.64 KB) Additional Information: [full citation](#), [abstract](#), [bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 90, Citation

Superscalar architectures have been proposed that exploit control independent instructions to avoid the performance penalty of branch mispredictions by preserving the work of independent instructions. The essential goal of exploiting control ...

Keywords: branch prediction, checkpoints, control independence, selection, recovery, speculation

Also published in:


June 2007 SIGARCH Computer Architecture News Volume 35 Issue 2

18 [MIL primitives for querying a fragmented world](#)

Peter A. Boncz, Martin L. Kersten

October 1999 The VLDB Journal — The International Journal on Very Large Databases
2

Publisher: Springer-Verlag New York, Inc.


Full text available:  Pdf (261.36 KB) Additional Information: [full citation](#), [abstract](#), [bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 49, Citation

In query-intensive database application areas, like decision support and data warehousing, vertical fragmentation have a significant performance advantage. In order to exploit this advantage, query oriented applications on top of such a fragmented ...


Keywords: Database systems, Main-memory techniques, Query languages, Vertical fragmentation

19 [Exploiting access semantics and program behavior to reduce snooping](#)

 Chinnakrishnan S. Ballapuram, Ahmad Sharif, Hsien-Hsin S. Lee

March 2008 ASPLOS XIII: Proceedings of the 13th international conference on architecture for programming languages and operating systems

Publisher: ACM


Full text available:  Pdf (226.27 KB) Additional Information: [full citation](#), [abstract](#), [bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 46, Downloads (12 Months): 334, Citation

Integrating more processor cores on-die has become the unanimous trend in the semiconductor industry. Most of the current research thrusts using chip multiprocessors are to analyze problems in various domains. One of the main design ...



Keywords: MESI protocol, chip multiprocessors, internal and external cache

20 [Fast triangle reordering for vertex locality and reduced overdraw](#)

 Pedro V. Sander, Diego Nehab, Joshua Barczak

August 2007 SIGGRAPH '07: SIGGRAPH 2007 papers

Publisher: ACM

Full text available:  [Mov](#) (25:35 MIN),  [Pdf](#) (2.74 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 26, Downloads (12 Months): 301, Citations: 1


We present novel algorithms that optimize the order in which triangles are transformed to transform vertex cache efficiency as well as for view-independent overdraw. Our triangle orders perform on par with previous methods, ...

Also published in:

July 2007 Transactions on Graphics (TOG) Volume 26 Issue 3

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